



Docket No.: 547-131

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Inventor(s): Pei-Pei DING et al.

U.S. Patent Application No. 10/670,190

Filed: September 26, 2003

:  
:  
: Confirmation No. 5418  
:  
: Group Art Unit: 1742  
:  
: Examiner:

For: METHOD OF MAKING A MICROSTRUCTURE USING A CIRCUIT BOARD

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. §1.56, §1.97 and §1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Respectfully submitted,

LOWE HAUPTMAN GILMAN & BERNER, LLP

Allan M. Lowe, Registration No. 19,641

1700 Diagonal Road, Suite 310  
Alexandria, Virginia 22314  
(703) 684-1111 AML/rk  
Facsimile: (703) 518-5499  
**Date: March 29, 2004**  
AML:rk

[illegible]

\* - European Search Report is in English.